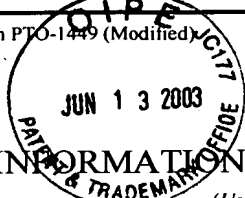


Form PTO-1449 (Modified)  INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 20002/37804	Serial No. 10/073,492
		Applicant Edirisooriya et al.	
		Filing Date 02/11/02	Group Art Unit 2185 2187

U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
BRP	5,996,049	11/30/99	Arimilli et al.	711	141	—	

FOREIGN PATENT DOCUMENTS							
*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes No	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
BRP	Tomasevic, M; Milutinovic, V. "Hardware Approaches to Cache Coherence in Shared-Memory Multiprocessors, Part 1." IEEE Micro. Vol. 14, Issue 5, pp 52-59. Oct. 1994.
BRP	Tomasevic, M; Milutinovic, V. "Hardware Approaches to Cache Coherence in Shared-Memory Multiprocessors, Part 2." IEEE Micro. Vol. 14, Issue 6, pp 61-66. Dec. 1994

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